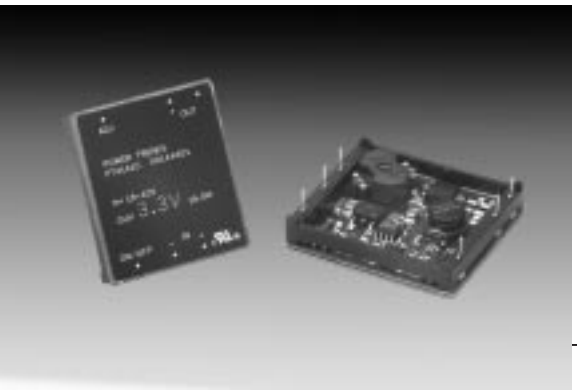
PT4140 Series—24V

20 Watt 24V Input Isolated DC-DC Converter

SLTS120A

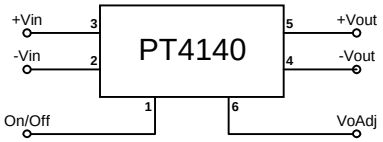
(Revised 2/15/2001)



- Wide Input Voltage Range 18V to 40V
- 82% Efficiency
- 1500 VDC Isolation
- Small Footprint 1.45" x 1.64"
- Low Profile 0.315" (8mm)
- Adjustable Output
- Short Circuit Protection
- Over Temperature Shutdown
- UL1950 Recognized
- CSA 22.2 950 Certified
- Designed to Meet EN60950

The PT4140 series of 24V Isolated DC-DC Converters advance the state-of-the-art for board-mounted converters by employing 850kHz switching frequencies, planar magnetics, and surface-mount construction. These regulators are pin-compatible with Power Trends' popular PT4100 series, and offer an improved power density (25 Watts/in³) plus output voltage adjustment. They are designed for Telecom, Industrial, Computer, Medical, and other distributed power applications that require input-to-output isolation.

Standard Application



Pin-Out Information

Pin	Function
1	Remote(3) ON/OFF
2	$-V_{in}$
3	$+V_{in}$
4	$-V_{out}$
5	$+V_{out}$
6	V_{out}Adjust(3)

Specifications

Characteristics (T_a = 25°C unless noted)	Symbols	Conditions	PT4140 SERIES Min	Typ	Max	Units
Output Current	I_o	Over V_{in} range $V_o \le 3.3V$	0	—	5.0 (2)	A
		V_o = 5V	0	—	4.0 (2)	
		V_o = 12V	0	—	1.6 (2)	
On/Off Standby Current	$I_{in\ standby}$	V_{in} = 24V, Pin 1 = $-V_{in}$	—	7	10	mA
Short Circuit Current	I_{sc}	V_{in} = 24V	—	2xI_{omax}	—	A
Inrush Current	I_{ir}	V_{in} = 24V @ $I_o$max	—	1.0	—	A
	t_{ir}	On start-up	—	1.0	—	mSec
Input Voltage Range	V_{in}	Over I_o Range	18.0	24.0	40.0	VDC
Output Voltage Tolerance	ΔV_o	Over V_{in} Range, I_o =$I_o$max T_a = -40°C to +85°C	—	±1.0	±2.0	%V_o
Line Regulation	Reg_{line}	Over V_{in} range @ $I_o$max	—	±0.5	±1.0	%V_o
Load Regulation	Reg_{load}	10% to 100% of I_o max	—	±0.5	±1.0	%V_o
V_o Ripple/Noise	V_n	V_{in}=24V, I_o=$I_o$max, $V_o \ge$ 5V	—	1.0	—	%V_o
		V_{in}=24V, I_o=$I_o$max, $V_o <$ 5V	—	50	—	mV_{pp}
Transient Response	t_{tr}	50% load change, $V_o \ge$ 5V	—	100	—	µSec
		V_o over/undershoot	—	3.0	—	%V_o
Efficiency	η	V_{in}=24V, I_o=5A, V_o=3.3V	—	78	—	%
		V_{in}=24V, I_o=4A, V_o=5V	—	82	—	
		V_{in}=24V, I_o=1.6A, V_o=12V	—	83	—	
Switching Frequency	f_o	Over V_{in} and I_o, $V_o \le$5V	800	850	900	kHz
		V_o >5V	600	650	700	
Maximum Operating Temperature Range	T_a	V_{in} = 24V with 200 LFM airflow	-40	—	+85 (2)	°C
Storage Temperature	T_s	—	-40	—	+110	°C
Mechanical Shock	—	Per Mil-Std-883D, method 2002.3, 1mS, half-sine, mounted to a fixture	—	500	—	G's
Mechanical Vibration	—	Per Mil-Std-883D, method 2007.2, 20-2000Hz, soldered in a PC board	—	15	—	G's
Weight	—	—	—	15	—	grams
Isolation	—	—	1500	—	—	V
Capacitance	—	—	—	1100	—	pF
Resistance	—	—	10	—	—	MΩ
Flammability	—	Materials meet UL 94V-0				
Remote On/Off	On	Open or 2.5 to 7.0 VDC above $-V_{in}$				
	Off	Short or 0 to 0.8 VDC above $-V_{in}$				

Notes: *(1) The maximumoutput current limits the power rating of the following devices to less than 20W:- PT4141 =16.5W; PT4146 =7.5W; PT4147 =9W; PT4148 =12.5W.*
(2) See Safe Operating Area curves or contact the factory for the appropriate derating.
(3) See Application Notes at end of section.

Ordering Information

(1) **PT4141☐** = 3.3V/5A
PT4142☐ = 5.0V/4A
PT4143☐ = 12.0V/1.6A
PT4144☐ = 15.0V/1.3A
(1) **PT4146☐** = 1.5V/5A
(1) **PT4147☐** = 1.8V/5A
(1) **PT4148☐** = 2.5V/5A

PT Series Suffix (PT1234X)

Pin Configuration	
Horizontal Through-Hole	**A**
Horizontal Surface Mount	**C**

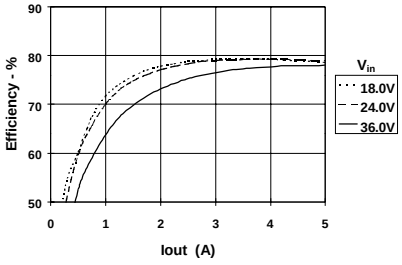
(For dimensions and PC board layout, see Package Style 710.)

*** Note:** This product is the subject of one or more patents. Other patents pending.

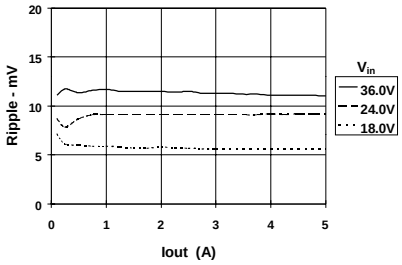
Typical Characteristics

PT4141, 3.3 VDC (See Note A)

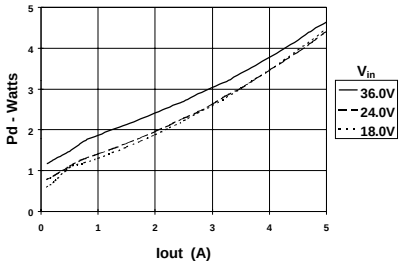
Efficiency vs Output Current



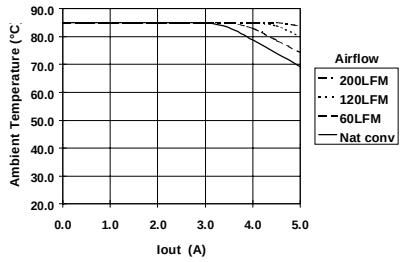
Ripple vs Output Current



Power Dissipation vs Output Current



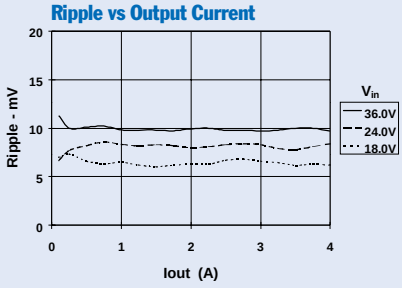
Safe Operating Area (See Note B)



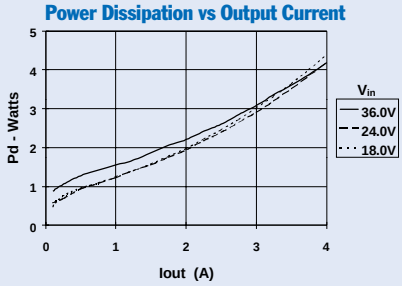
PT4142, 5.0 VDC (See Note A)

Efficiency vs Output Current

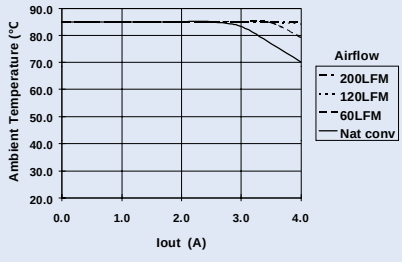
Ripple vs Output Current



Power Dissipation vs Output Current

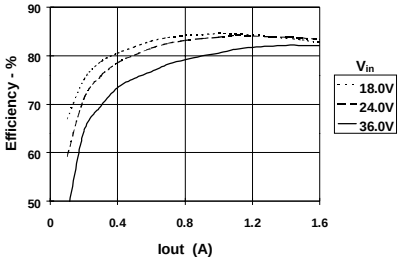


Safe Operating Area (See Note B)

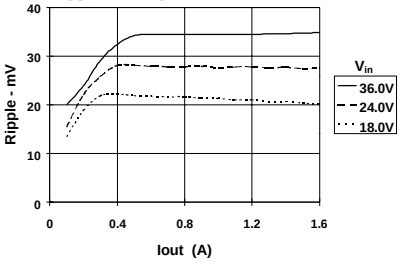


PT4143, 12.0 VDC (See Note A)

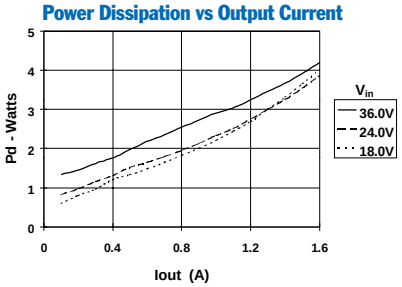
Efficiency vs Output Current



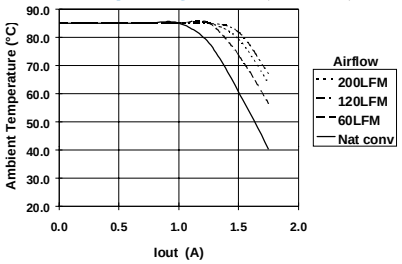
Ripple vs Output Current



Power Dissipation vs Output Current



Safe Operating Area (See Note B)



Note A: *Characteristic data has been developed from actual products tested at 25°C. This data is considered typical data for the Converter.*
Note B: *Safe Operating Area curves represent conditions at which internal components are at or below manufacturer's rated operating temperatures.*

Adjusting the Output Voltage of the PT4120/ PT4140 Series of Isolated DC-DC Converters

The factory pre-set output voltage of Power Trends' PT4120 and PT4140 series of isolated DC-DC converters may be adjusted within a nominal ±10% range. Adjustment is made from the secondary side of the regulator[1] with a single external resistor. For the input voltage range specified in the data sheet Table 1 gives the allowable adjustment range for each model, as V_o (min) and V_o (max).

Adjust Up: An increase in the output voltage is obtained by adding a resistor, R2 between pin 6 (V_o adjust), and pin 4 ($-V_{out}$).

Adjust Down: Add a resistor (R1), between pin 6 (V_o adjust) and pin 5 ($+V_{out}$).

Refer to Figure 1 and Table 2 for both the placement and value of the required resistor, (R1) or R2.

Notes:

1. The PT4120 and PT4140 series of dc-dc converters incorporate isolation between the V_{in} and V_o terminals. Adjustment of the output voltage is made to the regulation circuit on the secondary or output side of the converter.

2. The maximum rated output power for this series is 20W. An increase in the output voltage may therefore require a corresponding reduction in the maximum output current (*see Table 1*). The revised maximum output current must be determined as follows:-

$$I_o(\max) = \frac{20}{V_a} \text{ A}, \quad \text{or 5A, whichever is less.}$$

Where V_a is the adjusted ouput voltage.

3. Use only a single 1% resistor in either the (R1) or R2 location. Place the resistor as close to the ISR as possible.

4. Never connect capacitors to V_o adjust. Any capacitance added to the V_o adjust control pin will affect the stability of the ISR.

The values of (R1) [adjust down], and R2 [adjust up], can also be calculated using the following formulas.

$$(R1) = \frac{K_o(V_a - V_r)}{V_r (V_o - V_a)} - R_s \quad k\Omega$$

$$R2 = \frac{K_o}{(V_a - V_o)} - R_s \quad k\Omega$$

Where V_o = Original output voltage
V_a = Adjusted output voltage
V_r = Reference voltage (Table 1)
K_o = Multiplier constant (Table 1)
R_s = Internal series resistance (Table 1)

Figure 1

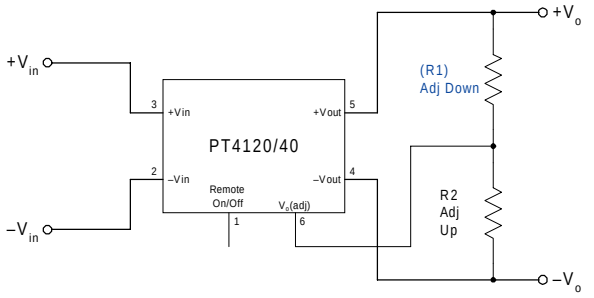


Table 1

DC-DC CONVERTER ADJUSTMENT RANGE AND FORMULA PARAMETERS

Series Pt #							
48V Bus	**PT4126**	**PT4127**	**PT4128**	**PT4121**	**PT4122**	**PT4123**	**PT4124**
24V Bus	**PT4146**	**PT4147**	**PT4148**	**PT4141**	**PT4142**	**PT4143**	**PT4144**
Max Current [2]	**5A**	**5A**	**5A**	**5A**	**4A**	**1.6A**	**1.3A**
V_o(nom)	1.5	1.8	2.5	3.3	5.0	12.0	15.0
Va(min)	1.35	1.62	2.25	2.95	4.5	10.8	13.5
Va(max)	1.65	1.98	2.75	3.65	5.5	13.2	16.5
V_r	1.225	1.225	1.225	1.225	2.5	2.5	2.5
K_o (V·kΩ)	67.07	69.7	64.2	69.3	125.2	139.8	137.6
R_S (kΩ)	43.2	110.0	187.0	187.0	187.0	110.0	90.9

Table 2

DC-DC CONVERTER SERIES ADJUSTMENT RESISTOR VALUES

Series Pt #								
48V Bus	**PT4126**	**PT4127**	**PT4128**	**PT4121**		**PT4122**		**PT4123**	**PT4124**
24V Bus	**PT4146**	**PT4147**	**PT4148**	**PT4141**		**PT4142**		**PT4143**	**PT4144**
V_o(nom)	**1.5Vdc**	**1.8Vdc**	**2.5Vdc**	**3.3Vdc**		**5.0Vdc**		**12.0Vdc**	**15.0Vdc**
V_a(req'd)					**V_a(req'd**		**V_a(req'd)**		
1.35	(2.8)kΩ				4.5	(12.6)kΩ	10.8	(276.0)kΩ	
1.4	(53.2)kΩ				4.55	(40.3)kΩ	11.0	(365.0)kΩ	
1.45	(204.0)kΩ				4.6	(75.0)kΩ	11.2	(497.0)kΩ	
1.5					4.65	(120.0)kΩ	11.4	(719.0)kΩ	
1.55	1.3MΩ				4.7	(179.0)kΩ	11.6	(1.16)MΩ	
1.6	627.0kΩ				4.75	(262.0)kΩ	11.8		
1.65	404.0kΩ	(51.7)kΩ			4.8	(387.0)kΩ	12.0		
1.7		(161.0)kΩ			4.85	(595.0)kΩ	12.2	588.0kΩ	
1.75		(489.0)kΩ			4.9	(1.01)MΩ	12.4	239.0kΩ	
1.8					4.95		12.6	123.0kΩ	
1.85		1.28MΩ			5.0		12.8	64.6kΩ	
1.9		587.0kΩ			5.05		13.0	29.7kΩ	
1.95		355.0kΩ			5.1	1.06MΩ	13.2	6.4kΩ	
2.25			(26.5)kΩ		5.15	645.0kΩ	13.5		(312.0)kΩ
2.3			(92.9)kΩ		5.2	437.0kΩ	13.6		(345.0)kΩ
2.35			(203.0)kΩ		5.25	312.0kΩ	13.8		(427.0)kΩ
2.4			(425.0)kΩ		5.3	229.0kΩ	14.0		(542.0)kΩ
2.45			(1.09)MΩ		5.35	169.0kΩ	14.2		(713.0)kΩ
2.5					5.4	125.0kΩ	14.4		(1.0)MΩ
2.55			1.09MΩ		5.45	90.2kΩ	14.6		(1.57)MΩ
2.6			450.0kΩ		5.5	62.4kΩ	14.8		
2.65			237.0kΩ				15.0		
2.7			131.0kΩ				15.2		597.0kΩ
2.75			67.7kΩ				15.4		253.0kΩ
2.95				(90.7)kΩ			15.6		138.0kΩ
3.0				(146.0)kΩ			15.8		81.0kΩ
3.05				(224.0)kΩ			16.0		46.6kΩ
3.1				(341.0)kΩ			16.5		0.8kΩ
3.15				(536.0)kΩ					
3.2				(926.0)kΩ					
3.25				(2.09)MΩ					
3.3									
3.35				1.19MΩ					
3.4				502.0kΩ					
3.45				272.0kΩ					
3.5				158.0kΩ					
3.55				88.7kΩ					
3.6				42.7kΩ					
3.65				9.9kΩ					

R1 = (Blue) R2 = Black

Using the Remote On/Off Function on the PT4120/ PT4140 Series of Isolated DC-DC Converters

For applications requiring output voltage on/off control, the PT4120/4140 series of DC-DC converters incorporate a remote on/off function. This function may be used in applications that require battery conservation, power-up/shutdown sequencing, and/or to co-ordinate the power-up of the regulator for active in-rush current control. (See the related application note, AN21).

This function is provided by the *Remote On/Off* control, pin1. If pin 1 is left open-circuit, the converter provides a regulated output whenever a valid source voltage[3] is applied between $+V_{in}$(pin 3), and $-V_{in}$ (pin 2). Applying a low-level ground signal [1] to pin 1 will disable the regulator output [5].

Table 1 provides details of the threshold requirements for *Remote On/Off* pin. Figure 1 shows how a discrete MOSFET (Q_1)[4], may be referenced to the negative input voltage rail and used with this control input.

Table 1 Inhibit Control Thresholds [2]

Parameter	min	max
Enable (VIH)	2.5V	(Open Circuit) 5
Disable (VIL)	–0.3V	0.8V

Notes:

1. The on/off control uses $-V_{in}$ (pin 2), the primary side of the converter as its ground reference. All voltages specified are with respect to $-V_{in}$.

2. The on/off control internal circuitry is a high impedance 10µA current source. The open-circuit voltage may be as high as 8.3Vdc.

3. The PT4120/40 series incorporates an "Under Voltage Lockout" (UVLO) function. This function automatically inhibits the converter output until there is sufficient input voltage for the converter to produce a regulated output. Table 2 gives the applicable UVLO thresholds.

Table 2 UVLO Thresholds [1]

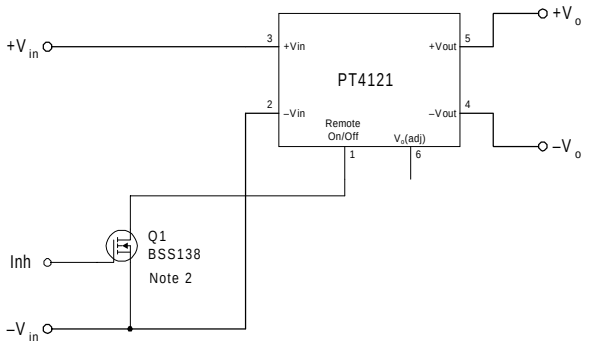
Series	UVLO Threshold	V_{in} Range
PT4120	31V Typical	36 – 75V
PT4140	15V Typical	18 – 40V

4. The *Remote On/Off* input of the PT4120/40 series regulators must be controlled with an open-collector (or open-drain) discrete transistor or MOSFET. *Do not* use a pull-up resistor.

5. When the converter output is disabled, the current drawn from the input supply is typically reduced to 8mA (16mA maximum).

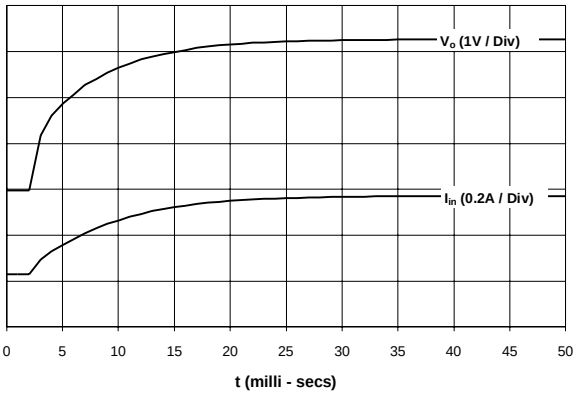
6. Keep the on/off transition to less than 1ms. This prevents erratic operation of the ISR, whereby the output voltage may drift un-regulated between 0V and the rated output during power-up.

Figure 1



Turn-On Time: The converter typically produces a fully regulated output voltage within 50ms after the application of power, or the removal of the low voltage signal from the *Remote On/Off* pin. The actual turn-on time will vary with the input voltage, output load, and the total amount of capacitance connected to the output. Using the circuit of Figure 1, Figure 2 shows the typical output voltage and input current waveforms of a PT4121 after Q_1 is turned off at time t = 0s. The waveform was measured with a 48Vdc input voltage, and 4A resistive load.

Figure 2



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